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7590

03/02/2004

**VOLENTINE FRANCOS, PLLC**  
 Suite 150  
 12200 Sunrise Vally Drive  
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/062,505	02/05/2002	Hiroyuki Kawano	OK1.304	2766

**TITLE OF INVENTION:** METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING ETCHING OF A PERIPHERAL AREA BEFORE CHEMICAL-MECHANICAL POLISHING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/02/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
GEYER, SCOTT B	2829	438-759000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. VOLENTINE FRANCOS, PLLC

2. \_\_\_\_\_

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Oki Electric Industry Co., Ltd.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) ANDREW J. TELESZ, JR. (Date) 05-28-04Dr #33,581

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**TRANSMITTAL OF PAYMENT OF ISSUE FEE (Large Entity)**  
**(37 C.F.R. 1.311)**

Docket No.  
**OKI.304**

Applicant(s): **Hiroyuki Kawano**

Serial No.

**10/062,505**

Filing Date

**February 5, 2002**

Examiner

**S. Geyer**

Group Art Unit

**2829**

Confirmation No.

**2766**

Invention: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING ETCHING OF A PERIPHERAL AREA BEFORE CHEMICAL-MECHANICAL POLISHING**

**Mail Stop Issue Fee**  
**TO THE COMMISSIONER FOR PATENTS**  
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Transmitted herewith are the following for the above-identified application.

- ☒ Issue Fee Transmittal Form PTOL-85
- ☒ Utility Fee: \$ 1330.00    ☐ Design Fee: \_\_\_\_\_    ☐ Plant Fee: \_\_\_\_\_
- ☒ Publication Fee: \$ 300.00
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*Signature*

Dated: **May 28, 2004**

**ANDREW J. TELESZ, JR.**  
**REG. NO. 33,581**

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